P.G. Diploma in Prospectus No.20151246
i) Mechatronics
ii) Biomedical Electronics, and
iii) Computer Maintenance

SANT GADGE BABA AMRAVATI UNIVERSITY

विज्ञान विद्याशाखा
(FACULTY OF SCIENCE)

PROSPECTUS
OF
The Examination for the Post Graduate Diploma in
i) Mechatronics,
ii) Biomedical Electronics, and
iii) Computer Maintenance
Semester-I, Winter-2014
Semester-II, Summer-2015 & onwards

2014
Visit us at www.sgbau.ac.in

Price Rs. ........./-

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Sant Gadge Baba
Amravati University
Amravati - 444 602
### Post Graduate Diploma in i) Mechatronics, ii) Biomedical Electronics and iii) Computer Maintenance

(One Year – Semester Pattern)

(Semester-I & II)

(Prospectus No. 20151246)

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Special Note</td>
<td>1</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>2</td>
<td>Pattern of Question Paper</td>
<td>2</td>
<td></td>
<td>2</td>
</tr>
<tr>
<td>3</td>
<td>Direction No.13/2012</td>
<td>3</td>
<td></td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>Direction No.5/2013</td>
<td>8</td>
<td></td>
<td>8</td>
</tr>
<tr>
<td>5</td>
<td>Direction No.1/2014</td>
<td>10</td>
<td></td>
<td>10</td>
</tr>
</tbody>
</table>

**Semester-I (Mechatronics)**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>6</td>
<td>1MCH1</td>
<td>I</td>
<td>Embedded System and Design</td>
<td>12</td>
</tr>
<tr>
<td>7</td>
<td>1MCH2</td>
<td>II</td>
<td>Robotics and Applications</td>
<td>13</td>
</tr>
<tr>
<td>8</td>
<td>1MCH3</td>
<td>III</td>
<td>Digital Image Processing</td>
<td>14</td>
</tr>
<tr>
<td>9</td>
<td>1MCH4</td>
<td>IV</td>
<td>Process Control</td>
<td>15</td>
</tr>
<tr>
<td>10</td>
<td>1MCH5</td>
<td>P-I</td>
<td>Practical I - LAB I</td>
<td>14</td>
</tr>
<tr>
<td>11</td>
<td>1MCH6</td>
<td>P-II</td>
<td>Practical II - LAB II</td>
<td>16</td>
</tr>
</tbody>
</table>

**Semester-II (Mechatronics)**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>12</td>
<td>2MCH1</td>
<td>V</td>
<td>Finite Element Techniques</td>
<td>16</td>
</tr>
<tr>
<td>13</td>
<td>2MCH2</td>
<td>VI</td>
<td>Industrial Tribology</td>
<td>18</td>
</tr>
<tr>
<td>14</td>
<td>2MCH3</td>
<td>VII</td>
<td>Design for Manufacture</td>
<td>19</td>
</tr>
<tr>
<td>15</td>
<td>2MCH4</td>
<td>VIII</td>
<td>Real Time Operating System</td>
<td>20</td>
</tr>
<tr>
<td>16</td>
<td>2MCH5</td>
<td>P-III</td>
<td>Practical - III LAB I/III</td>
<td>19</td>
</tr>
<tr>
<td>17</td>
<td>2MCH6</td>
<td>P-IV</td>
<td>Project</td>
<td>21</td>
</tr>
</tbody>
</table>

**Semester-I (Biomedical Electronics)**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>18</td>
<td>1BME1</td>
<td>I</td>
<td>Medical Physics</td>
<td>22</td>
</tr>
<tr>
<td>19</td>
<td>1BME2</td>
<td>II</td>
<td>Microprocessor, Microcontroller and Computer Applications in Medicine</td>
<td>23</td>
</tr>
<tr>
<td>20</td>
<td>1BME3</td>
<td>III</td>
<td>Human Physiology and Physiological Modelling</td>
<td>24</td>
</tr>
</tbody>
</table>

**Semester-II (Biomedical Electronics)**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>21</td>
<td>1BME4</td>
<td>IV</td>
<td>Biomedical Instrumentation</td>
<td>26</td>
</tr>
<tr>
<td>22</td>
<td>1BME5</td>
<td>P-I</td>
<td>Practical I - LAB I</td>
<td>24</td>
</tr>
<tr>
<td>23</td>
<td>1BME6</td>
<td>P-II</td>
<td>Practical II - LAB II</td>
<td>26</td>
</tr>
</tbody>
</table>

**Semester-I (Computer Maintenance)**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>30</td>
<td>1 CPMS 1</td>
<td>I</td>
<td>Basic Analog Electronics</td>
<td>31</td>
</tr>
<tr>
<td>31</td>
<td>1 CPMS 2</td>
<td>II</td>
<td>Basic Digital Electronics</td>
<td>33</td>
</tr>
<tr>
<td>32</td>
<td>1 CPMS 3</td>
<td>III</td>
<td>Computer Organization</td>
<td>34</td>
</tr>
<tr>
<td>33</td>
<td>1 CPMS 4</td>
<td>IV</td>
<td>Microprocessor &amp; Interfacing</td>
<td>34</td>
</tr>
<tr>
<td>34</td>
<td>1 CPMS 5</td>
<td>P-I</td>
<td>Practical I - LAB I</td>
<td>33</td>
</tr>
<tr>
<td>35</td>
<td>1 CPMS 6</td>
<td>P-II</td>
<td>Practical II - LAB II</td>
<td>35</td>
</tr>
</tbody>
</table>

**Semester-II (Computer Maintenance)**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>36</td>
<td>2 CPMS 1</td>
<td>V</td>
<td>C Language with ALP</td>
<td>35</td>
</tr>
<tr>
<td>37</td>
<td>2 CPMS 2</td>
<td>VI</td>
<td>Data Communication, Networking &amp; Internet</td>
<td>36</td>
</tr>
<tr>
<td>38</td>
<td>2 CPMS 3</td>
<td>VII</td>
<td>Networking &amp; Internet</td>
<td>37</td>
</tr>
<tr>
<td>39</td>
<td>2 CPMS 4</td>
<td>VIII</td>
<td>PC Maintenance &amp; Trouble shooting</td>
<td>38</td>
</tr>
<tr>
<td>40</td>
<td>2 CPMS 5</td>
<td>P-III</td>
<td>LAB I/III</td>
<td>37</td>
</tr>
<tr>
<td>41</td>
<td>2 CPMS 6</td>
<td>P-IV</td>
<td>LAB-IV</td>
<td>39</td>
</tr>
</tbody>
</table>
SANT GADGE BABA AMRAVATI UNIVERSITY

SPECIAL NOTE FOR INFORMATION OF THE STUDENTS

(1) Notwithstanding anything to the contrary, it is notified for general information and guidance of all concerned that a person, who has passed the qualifying examination and is eligible for admission only to the corresponding next higher examination as an ex-student or an external candidate, shall be examined in accordance with the syllabus of such next higher examination in force at the time of such examination in such subjects papers or combination of papers in which students from University Departments or Colleges are to be examined by the University.

(2) Be it known to all the students desirous to take examination/s for which this prospectus has been prescribed should, if found necessary for any other information regarding examinations etc., refer the University Ordinance Booklet the various conditions/provisions pertaining to examination as prescribed in the following Ordinances.

Ordinance No. 1 : Enrolment of Students.
Ordinance No. 2 : Admission of Students
Ordinance No. 4 : National cadet corps
Ordinance No. 6 : Examinations in General (relevant extracts)
Ordinance No. 18/2001 : An Ordinance to provide grace marks for passing in a Head of passing and Improvement of Division (Higher Class) and getting Distinction in the subject and condonation of deficiency of marks in a subject in all the faculties prescribed by the Statute No.18, Ordinance 2001.

Ordinance No. 9 : Conduct of Examinations (relevant extracts)
Ordinance No. 10 : Providing for Exemptions and Compartments
Ordinance No. 19 : Admission of Candidates to Degrees.

Ordinance No. 109 : Recording of a change of name of a University student in the records of the University.
Ordinance No. 6 of 2008 : For improvement of Division/Grade.
Ordinance No.19/2001 : An Ordinance for Central Assessment Programme, Scheme of Evaluation and Moderation of answerbooks and preparation of results of the examinations, conducted by the University, Ordinance 2001.

Registrar
Sant Gadge Baba Amravati University

PATTERN OF QUESTION PAPER ON THE UNIT SYSTEM

The pattern of question paper as per unit system will be boradly based on the following pattern.

(1) Syllabus has been divided into units equal to the number of question to be answered in the paper. On each unit there will be a question either a long answer type or a short answer type.

(2) Number of question will be in accordance with the unit prescribed in the syllabi for each paper i.e. there will be one question on each unit.

(3) For every question long answer type or short answer type there will be an alternative choice from the same unit. However, there will be no internal choice in a question.

(4) Division of marks between long answer and short answer type question will be in the ratio of 40 and 60.

(5) Each short answer type question shall Contain 4 to 8 short sub question with no internal choice.

*****
SANT GADGE BABA AMRAVATI UNIVERSITY, AMRAVATI

DIRECTION

No. : 13 / 2012

Subjects : Examinations leading to the Post - Graduate Diploma in (i) Mechatronics, (ii) Biomedical Electronics, and (iii) Computer Maintenance (Semester Pattern-One Year (Full Time) P.G. Diploma Course) in the faculty of Science, Directions, 2012.

Whereas, Govt. of Maharashtra, Department of Higher and Technical Education, vide its letter No. NGC-2011/(118/110), मंत्रिसं, वि.स. ऑफिसेर, 2011 has sanctioned approval for starting of Post - Graduate Diploma course in (i) Mechatronics, and (ii) Biomedical Electronics.

AND

Whereas, the Academic Council in its meeting held on 30.3.2011, vide item No.24(7) J) R-2, R-3 & R-4 has accepted the draft syllabi with scheme of examination, eligibility criteria, and other details for one year PG Diploma courses in (i) Computer Maintenance, (ii) Biomedical Electronics, and (iii) Mechatronics.

AND

Whereas, authority of this University has granted first time affiliation to Post Graduate Diploma course in (i) Mechatronics, and (ii) Biomedical Electronics from the Academic Session 2011-12 and communicated the same to the concern college vide this office letter No.SGBAU/8-C/A-271/2011, dt.25.10.2011.

AND

Whereas, Schemes of Examinations of above said courses, which is to be implemented from the Academic Session 2012-13 for Semester-I & II and onwards which are to be regulated by an Ordinance and making Ordinance is time consuming process.

AND

Whereas, the admission of students in the above said courses are to be made in the Academic Session 2012-13 in the affiliated college.

Now, therefore, I, Dr. Mohan K. Khedkar, Vice Chancellor, Sant Gadge Baba Amravati University, Amravati in exercise of powers conferred upon me under sub-section (8) of section 14 of the Maharashtra Universities Act., 1994, do hereby direct as under:

1) This Direction may be called "Examinations leading to the Post - Graduate Diploma in (i) Mechatronics, (ii) Biomedical Electronics, and (iii) Computer Maintenance (Semester Pattern-One Year (Full Time) P.G. Diploma Course) in the faculty of Science, Directions, 2011."

2) This Direction shall come into force from the date of its issuance.

3) Following shall be the Examinations leading to the Post - Graduate Diploma in-
   (i) Mechatronics/Biomedical Electronics/Computer Maintenance, Semester-I Examination.
   (ii) Mechatronics/Biomedical Electronics/Computer Maintenance, Semester-II Examination.

4) Duration of each of the above semester shall be six months with an examination at the end of each semester.

5) The examinations specified in paragraph 3 above shall be held twice a year at such places and on such dates as may be appointed by the Board of Examinations.
   (i) The examinations specified in paragraph 3 above shall be held twice a year at such places and on such dates as may be appointed by the Board of Examinations.
   (ii) Main Examination of Semester-I shall be held in Winter and Supplementary Examination in Summer. (ii) Main Examination of Semester-I & Semester-III shall be held in Winter and Supplementary Examination in Summer.
   (iii) Main Examination of Semester-II shall be held in Summer and Supplementary Examination in Winter. (iii) Main Examination of Semester-II & Semester-IV shall be held in Summer and Supplementary Examination in Winter.

6) Subject to his/her compliance with the provisions of this Direction and of other Ordinances in force from time to time, the following candidates shall be eligible for admission to the Post-Graduate Diploma in-
   (i) Mechatronics
      a) A person who has passed B.Sc. examination with Electronics or Computer Science of this University or any other statutory University recognized equivalent by the Sant Gadge Baba Amravati University.
      OR
      b) A person who has passed Bachelor of Engineering (Electronics and Power System/Industrial Electronics/Mechanical Engineering) Degree of this University or any other statutory University recognized equivalent by the Sant Gadge Baba Amravati University.
   (ii) Biomedical Electronics
      a) A person who has passed B.Sc. examination with Electronics or Physics of this University or any other statutory University recognized equivalent by the Sant Gadge Baba Amravati University.
      OR
      b) A person who has passed Bachelor of Engineering (Electronics Discipline) Degree of this University or any other statutory University recognized equivalent by the Sant Gadge Baba Amravati University.
(iii) **Computer Maintenance**

a) A person who has passed B.Sc. examination with Mathematic/Physics/Electronics/Computer Science/Information Technology/Computer Application (Regular/Vocational)/Computer Maintenance of this University or any other statutory University recognized equivalent by the Sant Gadge Baba Amravati University.

**OR**

b) A person who has passed Bachelor of Computer Application Degree of Sant Gadge Baba Amravati University only.

7) Subject to his/her compliance with the provisions of this Direction and of other Ordinances (Pertaining to examination in General) in force from time to time, the applicant for admission to examination at the end of the course of study of a particular Semester shall be eligible to appear at it, if: (i) he/she satisfied the conditions in the table and the provisions thereunder :

<table>
<thead>
<tr>
<th>Sr.No.</th>
<th>Name of examination</th>
<th>The student should have completed the term satisfactorily</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Diploma in Mechatronics/Biomedical Electronics/Computer Maintenance Semester-I</td>
<td>Semester-I</td>
</tr>
<tr>
<td>2</td>
<td>Diploma in Mechatronics/Biomedical Electronics/Computer Maintenance Semester-II</td>
<td>Semester-II</td>
</tr>
</tbody>
</table>

**(Note**: Subjects prescribed and numbered in the scheme of Examinations shall be treated as separate subjects, however, the theory and practical, if any, of the subject shall be treated as separate Head of Passing.)

(ii) He/She has complied with provisions of Ordinance pertaining to Examination in general.

(iii) He/She has prosecuted a regular course of study in University Department/College affiliated to the University.

(iv) He/She has in the opinion of the Head of the Department/Principal, shown satisfactory progress in his/her studies.

8) Papers and the Practicals in which an examinee is be to examined, maximum marks for these and the minimum pass marks which an examinee must obtain in order to pass in the subjects and the examination shall be as indicated in Appendices-\(\Delta\) b\(\Delta\) & c\(\Delta\) appended with this Ordinance for P.G.Diploma course in Mechatronics, Biomedical Electronics, Computer Maintenance.

9) Examination fees for each semester of the examination and also the practical examination shall be as prescribed by the University from time to time.

10) An examinee who is successful at Semester-I, Semester-II examinations under this Ordinance and who has obtained 75% or more marks in aggregate of Semester-I, Semester-II Examinations shall be placed in the First Division with Distinction, those obtaining 60% or more but less than 75% shall be placed in the First Division and all other successful examinees shall be placed in the Second Division.

11) (i) Scope of the subjects shall be as indicated in the syllabus.

(ii) Medium of instruction and examination shall be English.

12) Provision of Ordinance No.18 of 2001 relating to an Ordinance to provide grace marks for passing in a head of passing and Improvement of Division (Higher Class) and getting distinction in the subject and condonation of deficiency of marks in a subject in all the faculties prescribed by the Statute No.18 and of Ordinance No.10 relating to Providing for Exemptions and Compartments shall apply to the examination under this Ordinance.

13) An examinee who does not pass or who fails to present himself/herself for the examination shall be eligible for readmission to the same examination on payment of fresh fees and such other fees as may be prescribed.

14) As soon as possible after the examinations, the Board of Examination shall publish a result of the examinees. The result of the examinations shall be classified as above and merit list shall be notified as per Ordinance No. 6

15) Notwithstanding anything to the contrary in this Ordinance, no one shall be admitted to an examination under this Ordinance, if he/she has already passed the same examination or an equivalent examination of any Statutory University.

16) Examinees successful at Post-Graduate Diploma Semester-II Examination shall on payment of prescribed fees, receive a Diploma in the prescribed form signed by the Vice-Chancellor.

Amravati

Date: 12/04/2012

Sd/-

(Dr. M. K. Khedkar)
Vice-Chancellor
### Scheme of Teaching and Examination for the Post Graduate Diploma in Mechatronics (One Year - Semester Pattern)

<table>
<thead>
<tr>
<th>Sr No</th>
<th>Sub Code No</th>
<th>Paper / Practical No.</th>
<th>Subject</th>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Theory</td>
<td>Practical</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Periods / week</td>
<td>Duration of Papers (Hrs.)</td>
</tr>
<tr>
<td>1</td>
<td>1MCH1</td>
<td>I</td>
<td>Embedded System and Design</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>1MCH2</td>
<td>II</td>
<td>Very Large Scale Integrated Circuit Design</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>3</td>
<td>1MCH3</td>
<td>III</td>
<td>Digital Image Processing</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>1MCH4</td>
<td>IV</td>
<td>Electronics Manufacturing Technology</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>1MCH5</td>
<td>P-I</td>
<td>Practical I - LAB I</td>
<td>-</td>
<td>6</td>
</tr>
<tr>
<td>6</td>
<td>1MCH6</td>
<td>P-II</td>
<td>Practical II - LAB II</td>
<td>-</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Total</td>
</tr>
</tbody>
</table>

### Semester II

<table>
<thead>
<tr>
<th>Sr No</th>
<th>Sub Code No</th>
<th>Paper / Practical No.</th>
<th>Subject</th>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Theory</td>
<td>Practical</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Periods / week</td>
<td>Duration of Papers (Hrs.)</td>
</tr>
<tr>
<td>1</td>
<td>2MCH1</td>
<td>V</td>
<td>Finite Element Techniques</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>2MCH2</td>
<td>VI</td>
<td>Industrial Tribology</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>3</td>
<td>2MCH3</td>
<td>VII</td>
<td>Design for Manufacture</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>2MCH4</td>
<td>VIII</td>
<td>Real Time Operating System</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>2MCH5</td>
<td>P-III</td>
<td>Practical - III LAB – III</td>
<td>-</td>
<td>6</td>
</tr>
<tr>
<td>6</td>
<td>2MCH6</td>
<td>P-IV</td>
<td>Project</td>
<td>-</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Total</td>
<td>16</td>
</tr>
</tbody>
</table>
### Appendix-B

**Scheme of Teaching and Examination for the Post Graduate Diploma in Bio-Medical Electronics (One Year - Semester Pattern)**

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Sub. Code No.</th>
<th>Paper / Practical No.</th>
<th>Subject</th>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
<th>Grand Total</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Theory</td>
<td>Practical</td>
<td>Total Periods / week</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Theory Marks</td>
<td>Practicals</td>
<td>All</td>
</tr>
<tr>
<td>1</td>
<td>1BME1</td>
<td>I</td>
<td>Medical Physics</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>2</td>
<td>1BME2</td>
<td>II</td>
<td>Microprocessor, Microcontroller and Computer Applications in Medicine</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>3</td>
<td>1BME3</td>
<td>III</td>
<td>Human Physiology and Physiological Modelling</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>4</td>
<td>1BME4</td>
<td>IV</td>
<td>Principles of Communications</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>5</td>
<td>1BME5</td>
<td>P-I</td>
<td>Practical I - LAB I</td>
<td>-</td>
<td>6</td>
<td>6</td>
</tr>
<tr>
<td>6</td>
<td>1BME6</td>
<td>P-II</td>
<td>Practical II - LAB II</td>
<td>-</td>
<td>6</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td></td>
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<td></td>
<td>Total</td>
<td>16</td>
<td>12</td>
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<td></td>
<td></td>
<td></td>
<td>Semester I</td>
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<td></td>
<td></td>
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<tr>
<td></td>
<td></td>
<td></td>
<td>Semester II</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>2BME1</td>
<td>V</td>
<td>Signal Systems and Networks</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>7</td>
<td>2BME7</td>
<td>VI</td>
<td>Biomedical Instrumentation</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>3</td>
<td>2BME3</td>
<td>VII</td>
<td>Sensors and Measuring Techniques</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>4</td>
<td>2BME4</td>
<td>VIII</td>
<td>Multimedia and Medical Informatics</td>
<td>4</td>
<td>-</td>
<td>4</td>
</tr>
<tr>
<td>5</td>
<td>2BME5</td>
<td>P-III</td>
<td>Practical - III LAB -III</td>
<td>-</td>
<td>6</td>
<td>6</td>
</tr>
<tr>
<td>6</td>
<td>2BME6</td>
<td>P-IV</td>
<td>Practical IV LAB IV</td>
<td>-</td>
<td>6</td>
<td>6</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Total</td>
<td>16</td>
<td>12</td>
</tr>
</tbody>
</table>

Grand Total of Semester I & II: **600**
### Appendix-C

Scheme of Teaching and Examination for the Post Graduate Diploma in Computer Maintenance (One Year - Semester Pattern)

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Sub Code No.</th>
<th>Paper / Practical No.</th>
<th>Subject</th>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
<th>Practical</th>
<th>Total</th>
<th>Pass Mark</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Theory</td>
<td>Practical</td>
<td>Trick Periods / Week</td>
<td>Pass Marks Internal Assessment</td>
<td>Total</td>
</tr>
<tr>
<td>1</td>
<td>2</td>
<td>3</td>
<td>4</td>
<td>5</td>
<td>6</td>
<td>7</td>
<td>8</td>
<td>9</td>
</tr>
<tr>
<td>Semester-I</td>
<td></td>
<td></td>
<td></td>
<td>Theory</td>
<td>Practical</td>
<td>Trick Periods / Week</td>
<td>Pass Marks Internal Assessment</td>
<td>Total</td>
</tr>
<tr>
<td>1</td>
<td>1 CPMS 1</td>
<td>I</td>
<td>Basic Analog Electronics</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>2</td>
<td>1 CPMS 2</td>
<td>II</td>
<td>Basic Digital Electronics</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>3</td>
<td>1 CPMS 3</td>
<td>III</td>
<td>Computer Organization</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>4</td>
<td>1 CPMS 4</td>
<td>IV</td>
<td>Microprocessor &amp; Interfacing</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>5</td>
<td>1 CPMS 5</td>
<td>V</td>
<td>Practical I - LAB I</td>
<td>-</td>
<td>6</td>
<td>6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>6</td>
<td>1 CPMS 6</td>
<td>P-II</td>
<td>Practical II - LAB II</td>
<td>-</td>
<td>6</td>
<td>6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Total</td>
<td>16</td>
<td>12</td>
<td>28</td>
<td>200</td>
<td></td>
</tr>
<tr>
<td>Semester-II</td>
<td></td>
<td></td>
<td></td>
<td>Theory</td>
<td>Practical</td>
<td>Trick Periods / Week</td>
<td>Pass Marks Internal Assessment</td>
<td>Total</td>
</tr>
<tr>
<td>1</td>
<td>2 CPMS 1</td>
<td>V</td>
<td>C Language with ALP</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>2</td>
<td>2 CPMS 2</td>
<td>VI</td>
<td>Data Communication, Networking &amp; Internet</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>3</td>
<td>2 CPMS 3</td>
<td>VII</td>
<td>Networking &amp; Internet</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>4</td>
<td>2 CPMS 4</td>
<td>VIII</td>
<td>PC Maintenance &amp; Trouble Shooting</td>
<td>4</td>
<td>-</td>
<td>4</td>
<td>3</td>
<td>40</td>
</tr>
<tr>
<td>5</td>
<td>2 CPMS 5</td>
<td>P-III</td>
<td>LAB -III</td>
<td>-</td>
<td>6</td>
<td>6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>6</td>
<td>2 CPMS 6</td>
<td>P-IV</td>
<td>LAB-IV</td>
<td>-</td>
<td>6</td>
<td>6</td>
<td>-</td>
<td>-</td>
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<tr>
<td></td>
<td></td>
<td></td>
<td>Total</td>
<td>16</td>
<td>12</td>
<td>28</td>
<td>200</td>
<td></td>
</tr>
</tbody>
</table>

Grand Total of Semester-I & II: 600
SANT GADGE BABA AMRAVATI UNIVERSITY, AMRAVATI
DIRECTION

No. : 5 of 2013 Date: 09/05/2013

Subject: Corrigendum to Direction in respect of Examinations leading to the Post-Graduate Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics, and (iii) Computer Maintenance [Semester Pattern.....One Year (Full Time) P.G. Diploma Course] in the faculty of Science, Direction, 2013.

Whereas, Direction No.13 of 2012 in respect of Examinations leading to the Post-Graduate Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics, and (iii) Computer Maintenance [Semester Pattern (One Year) (Full Time) P.G. Diploma Course in the Faculty of Science, is in existence in the University.

AND

Whereas, the provision regarding "on job training" is already in existence in the syllabi of P.G.Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics in lieu of Second Semester for which distribution of marks was not prescribed in the scheme of examination.

AND

Whereas, taking into consideration the need of establishing the scheme of awarding the marks for "on job training" the Academic Council in its meeting held on 18.4.2013 vide item No.2 H) in respect of recommendations of Faculty of Science, accepted the distribution of marks for "on job training" and resolved to refer the matter to Ordinance Committee.

AND

Whereas, the matter is required to be regulated by framing the Ordinance and making of an Ordinance may likely to take some time.

AND

Whereas, the student appearing for "on job training" in lieu of Second Semester in the academic session 2012-13 shall require the distribution of marks.

Now, therefore, I, Dr. Mohan K. Khedkar, Vice-Chancellor of Sant Gadge Baba Amravati University, Amravati in exercise of powers conferred upon me under sub-section (8) of section 14 of the Maharashtra Universities Act, 1994, do hereby direct as under:

1) This Direction may be called, "Corrigendum to Direction in respect of Examinations leading to the Post-Graduate Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics, and (iii) Computer Maintenance [Semester Pattern.....One Year (Full Time) P.G. Diploma Course] in the Faculty of Science, Direction, 2013".

2) This Direction shall come into force w.e.f. the date of its issuance.

3) In Direction No.13 of 2012, at the end of Appedix-A & B in respect of Schemes of Examinations leading to the Post-Graduate Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics, the following note and scheme for distribution of marks be added.

On job training - Total marks 300

Date: 7/5/2013

Sd/-

(Vice-Chancellor)
SANT GADGE BABA AMRAVATI UNIVERSITY, AMRAVATI

DIRECTION

No. : 1 of 2014

Date: 21/02/2014

Subject: Corrigendum to Direction No. 13 of 2012 in respect of Examinations leading to the Post-Graduate Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics, and (iii) Computer Maintenance [Semester Pattern.....One Year (Full Time) P.G. Diploma Course] in the faculty of Science, Direction, 2013.

Whereas, Direction No.13 of 2012 in respect of Examinations leading to the Post-Graduate Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics, and (iii) Computer Maintenance [Semester Pattern.....One Year (Full Time) P.G. Diploma Course] in the Faculty of Science, is in existence in the University.

AND

Whereas, the B.O.S. in Electronics in its meeting held on 28.8.2013 while considering the item in respect of Proposals regarding changes in Syllabi resolved to recommend the changes in the syllabi of P.G. Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics to be implemented from the Academic Session 2013-14.

AND

Whereas, the Hon’ble Vice-Chancellor has accepted the above recommendations of B.O.S. u/s 14(7) of the Maharashtra Universities Act, 1994 on behalf of Faculty of Science on dated 30.8.2013.

AND

Whereas, the Academic Council in its meeting held on 31.8.2013 has approved the above recommendations.

AND

Whereas, the above changes in syllabi are related to change in the paper title which are prescribed in Appendices-A & B appended to Direction No.13 of 2012.

AND

Whereas, the matter is required to be regulated by framing the Ordinance and making of an Ordinance may likely to take some time.

AND

Whereas, the changes are to be made applicable from the Academic Session 2013-14.

Now, therefore, I, Dr. Jaikiran Tidke, Vice-Chancellor of Sant Gadge Baba Amravati University, Amravati in exercise of powers conferred upon me under sub-section (8) of section 14 of the Maharashtra Universities Act, 1994, do hereby direct as under:

1) This Direction may be called, “Corrigendum to Direction in respect of Examinations leading to the Post-Graduate Diploma in (i) Mechatronics, (ii) Bio-Medical Electronics, and (iii) Computer Maintenance [Semester Pattern.....One Year (Full Time) P.G. Diploma Course] in the Faculty of Science, Direction, 2014”

2) This Direction shall come into force w.e.f. the date of its issuance.

3) In Direction No.13 of 2012, in Appendix-A in respect of Scheme of Examinations leading to the Post-Graduate Diploma in Mechatronics,

   i) The title of Paper 1MCH2, ‘Very Large Scale Integrated Circuit Design’ be substituted by the title ‘Robotics and Applications’

   ii) The title of Paper 1MCH4, ‘Electronics Manufacturing Technology’ be substituted by the title ‘Process Control’

4) In Direction No.13 of 2012, in Appendix-B in respect of Scheme of Examinations leading to the Post-Graduate Diploma in Bio-Medical Electronics, the title of Paper 1BME4, ‘Principles of Communications’ be substituted by the title ‘Biomedical Instrumentation’

Date: 20/2/2014

(Dr. Jaikiran Tidke)
Vice-Chancellor
Sant Gadge Baba Amravati University
SYLLABUS PRESCRIBED FOR POST GRADUATE
DIPLOMA IN MECHATRONICS

SEMESTER-I

PAPER I

1MCH1 : Embedded System and Design

UNIT-I : BASICS OF EMBEDDED SYSTEMS AND HARDWARE
REQUIREMENTS : Introduction - Examples of embedded systems: Telegraph - Cordless bar - Code scanner - Laser printer - Underground tank monitoring - Nuclear Reactor monitor. Advanced hardwares: Programmable array logic; Application Specific Integrated circuits (ASIC) and Field Programmable Gate arrays (FPGA) - Watch dog timers - Built - Ins on the microprocessor - Interrupt.

UNIT-II : EMBEDDED SOFTWARE ARCHITECTURE: Round robin - Round robin with interrupts - Function Queue scheduling Architecture - Real time operating systems Architecture - Selecting architecture

REAL TIME OPERATING SYSTEM: Tasks and Task states - Tasks and Data - Semaphore and shared data - Timer functions - Events - Memory management - Interrupt routines in an RTOS Environment.

Design of an embedded system.

UNIT-III : EMBEDDED SOFTWARE DEVELOPMENT TOOLS AND DEBUGGING : Linker/Locators for embedded software - embedded software in to the target system - Testing on host machine: Basic techniques - more advanced techniques - Limitations and shortcomings - Instruction set simulators - The assert macro - Testing using laboratory tools.

UNIT-IV : Mechatronics : Defination, design process, Closed loop Controllers: continuous and discrete control processes, Terminology, Two step mode, Proportional mode, Derivative control, Integral control, PID controller, Digital controllers, Controller tuning, velocity control, Adaptive control.

UNIT-V : Programmable Logic Controller: Programmable logic controller, basic PLC structure, I/P- O/P Processing, Ladder programming, Instruction lists, latching and integral relays, sequencing, timers and Counters, Shift registers, Master and jump controls, data handling.

Text Book :

PAPER II

1MCH2 : Robotics and Applications


Text Books:
1. Automation and Robotics by Miltiadis A. Roboulos
2. Industrial Robotics: Theory, modelling and Control by Sam Cubero
PAPER III

1MCH3 : Digital Image Processing

UNIT I : DIGITAL IMAGE FUNDAMENTALS :- Elements of digital image processing systems, Elements of Visual perception, Image sampling and quantization, Matrix and Singular Value representation of discrete images.

UNIT II : IMAGE TRANSFORMS :- 1D DFT, 2D DFT, Cosine, Sine, Hadamard, Haar, Slant, KL, SVD transforms and their properties.

UNIT III : IMAGE ENHANCEMENT :- Histogram Modification and specification techniques, Image smoothing, Image sharpening, generation of spatial masks from frequency domain specification, Nonlinear filters, Homomorphic filtering, false color, Pseudocolor and color image processing.


UNIT V : IMAGE COMPRESSION :- Runlength, Huffman coding, Shift codes, arithmetic coding, bit plane coding, transform coding, JPEG Standard, wavelet transform, predictive techniques, Block truncation coding schemes, Facet modeling.

Text Books:


References:

3. Umbaugh, COMPUTER VISION Fī

PAPER IV

1MCH4 : Process Control


UNIT III : Non electrical Measurement system. Rotameter, Nozel & pitot tubes, Manometers, Float type & Air purge method of level measurement. Bimetallic thermometers, mercury/ Alcohol in glass thermometers


Text Books:
1. Fundamental of Industrial Instrumentation and Process Control by William C. Dunn
2. Process equipment Malfunction by Norman P. Liberman
3. Fundamentals of Programmable Logic Controllers, sensors and communication by John Stenerson
4. Data Communication for Instrumentation and control by John Park, Steve Mackay, Edwin Weight

LAB II:

PRACTICALS: Atleast 8 practicals to be performed by each student based on paper III & IV

SYLLABUS PRESCRIBED FOR POST GRADUATE DIPLOMA IN MECHATRONICS

SEMESTER- II

PAPER V

2MCH1 : Finite Element Techniques

UNIT-I : INTRODUCTION - VARIATIONAL FORMULATION

UNIT-II : FINITE ELEMENT ANALYSIS OF ONE DIMENSIONAL PROBLEMS
One dimensional second order equations - discretisation of domain into elements - Generalised coordinates approach - derivation of elements equations - assembly of element equations - imposition of boundary conditions - solution of equations - Cholesky method - Post processing - Extension of the method to fourth order equations and their solutions - time dependent problems and their solutions - example from heat transfer, fluid flow and solid mechanics.

UNIT-III : FINITE ELEMENT ANALYSIS OF TWO DIMENSIONAL PROBLEMS
Second order equations involving a scalar-valued function - model equation - Variational formulation ñ Finite element formulation through generalised coordinates approach - Triangular elements and quadrilateral elements - convergence criteria for chosen models - Interpolation functions - Elements matrices and vectors - Assembly of element matrices - boundary conditions - solution techniques.

UNIT-IV : ISOPARAMETRIC ELEMENTS AND FORMULATION
Natural coordinates in 1,2 and 3 dimensions - use of area coordinates for triangular elements in - 2 dimensional problems - Isoparametric elements in 1,2 and 3 dimensions - Lagrangean and serendipity elements - Formulation of element equations in one and two dimensions - Numerical integration.

UNIT-V : APPLICATIONS TO FIELD PROBLEMS IN TWO DIMENSIONS

Text Book:

References:
PAPER VI
2MCH2 :Industrial Tribology


Text Book:

References:

LAB III
PRACTICALS : Atleast 8 practicals to be performed by each student based on paper V & VI

PAPER VII
2MCH3 : Design for Manufacture

UNIT-I : DFMN APPROACH AND PROCESS Methodologies and tools, design axioms, design for assembly and evaluation, minimum part assessment tauchchi method, robustness assessment, manufacturing process rules, designer tool kit, Computer Aided group process rules, designer tool kit, Computer Aided group Technology, failure mode effective analysis, Value Analysis. Design for minimum number of parts, development of modular design, minimising part variations, design of parts to be multi-functional, multi-use, ease of fabrication, Poka Yoka principles.

UNIT-II : GEOMETRIC ANALYSIS Process capability, feature tolerance, geometric tolerance, surface finish, review of relationship between attainable tolerance grades and difference machining processes. Analysis of tapers, screw threads, applying probability to tolerences.

UNIT-III : FORM DESIGN OF CASTINGS AND WELDMENTS Redesign of castings based on parting line considerations, minimising core requirements, redesigning cast members using weldments, use of welding symbols.

UNIT-IV : MECHANICAL ASSEMBLY Selective assembly, deciding the number of groups, control of axial play, examples, grouped datum systems - different types.
geometric analysis and applications-design features to facilitate automated assembly.

UNIT-V : TRUE POSITION THEORY
Virtual size concept, floating and fixed fasteners, projected tolerance zone, assembly with gasket, zero true position tolerance, functional gauges, paper layout gauging, examples. Operation sequence for typical shaft type of components. Preperation of process drawings for different operations, tolerance worksheets and centrality analysis, examples.

Text Books:

References:

PAPER VIII
2MCH4 : Real Time Operating System


UNIT-V : CONNECTIVITY

References:

Project : Based on above syllabus.

NOTE : After completion of first semester, Students have to complete either second semester or the on job training in company.

1. Marks on Academic Performance - 50
   a) Attendance - 10
   b) Knowledge of concepts - 10
   c) Intellectual Ability - 05
   d) Decision making ability - 05
   e) Skill for handling the task - 10
   f) Co-operation/leadership qualities - 05
   g) Sense of Responsibility - 05

2. Presentation and Demonstration of the Project Completed during training. - 100
3. Viva-voce (Internal at industries) - 50
4. Evaluation of Project (External) - 100

*****
SYLLABUS PRESCRIBED FOR P. G. DIPLOMA IN BIO-MEDICAL ELECTRONICS

SEMESTER- I

Paper I

1BME1 : MEDICAL PHYSICS


UNIT-II : INTERACTION WITH LIVING CELLS :- Target theory, single hit and multi target theory, cellular effects of radiation, DNA damage, depression of Macro molecular synthesis, Chromosomal damage.

UNIT-III : SOMATIC EFFECT OF RADIATION :- Radio sensitivity protocol of different tissues in human, LD 50/30 effect of radiation on skin, blood forming organs, lenses of eye, embryo and Endocrinal glands.


UNIT-V : PHOTOMEDICINE :- Synthesis of Vitamin D in early and late cutaneous effects, Phototherapy, Photo hemotherapy, exposure level, hazards and maximum permissible exposures.

LASER PHYSICS - Characteristics of Laser radiation, Laser speckle, biological effects, laser safety management.

TEXT BOOKS


REFERENCES

1. Glasser, O. Medical Physics Vol. 1,2,3 year Book Publisher Inc Chicago, 1980

PAPER II

1BME2 : MICROPROCESSOR, MICROCONTROLLER AND COMPUTER APPLICATIONS IN MEDICINE

UNIT-I : 8-BIT MICROPROCESSOR AND 80X86 PROCESSORS:
8085 Architecture and Memory interfacing, interfacing I/O devices, Instruction set, Addressing Modes, Assembly language programming, counters and time delays, interrupts, timing diagram, Microprocessor applications. 8086 Architecture, Pin Configuration, 8086 Minimum and Maximum mode configurations, Addressing modes, Basic Instructions, 8086 Interrupts, Assembly levels programming. Introduction to 80186, 80286, 80386, 80486 and Pentium processors.

UNIT-II : MICROPROCESSOR PERIPHERALS AND INTERFACING:
Serial and parallel I/O (8251 and 8255), Programmable DMA Controller (8257), Programmable interrupt controller (8259), keyboard display controller (8279), ADC/DAC interfacing. Inter integrated circuits interfacing (I2C standard). Interfacing to alpha numeric displays, interfacing to liquid crystal display (LCD 16 x 2 line), high power Devices and Optical motor shaft encoders, stepper motor interfacing, Analog interfacing and industrial control, microcomputer based smart scale, industrial process control system, Robotics and Embedded control, DSP and Digital Filters.

UNIT-III : MICROCONTROLLER:
Intel 8031/8051 Architecture, Special Function Registers (SFR), I/O pins, ports and circuits, Instruction set, Addressing Modes, Assembly Language Programming, Timer and Counter Programming, Serial Communication, Connection to RS232, Interrupts Programming, External Memory interfacing, Introduction to 16 bit Microcontroller

UNIT-IV : SYSTEM DESIGN & COMPUTERS IN PATIENT MONITORING:
Multichannel computerised ECG, EMG and EEG data acquisition, storage and retrieval, transmission of signal and images.
Physiological monitoring, automated ICU, computerised arrhythmia monitoring, information flow in a clinical lab, computerised concepts, interfacing to HIS.


REFERENCES

LAB-I

PRACTICALS : Atleast 8 practicals to be performed by each student based on paper I & II

PAPER III

1BME3 : HUMAN PHYSIOLOGY & PHYSIOLOGICAL MODELLING


UNIT-IV : INTRODUCTION & TRANSFER FUNCTIONS: System concept, system properties, piece-wise linear approximation, electrical analog for compliance, thermal storage, pulse response of first order systems, response of resistant and compliance system. Transfer functions and its use, engineering concept in coupled system, example of Transformed signals.

UNIT-V : IMPEDANCE CONCEPT FEEDBACK SYSTEMS SIMULATION OF BIOLOGICAL SYSTEMS: Circuits for the Transfer function with impedance concept, prediction of performance, periodic signals. Characteristics of physiological feedback systems, uses and testing of system stability. Simulation of thermal regulation, pressure and flow control in circulation, occulo motor system, endocrinal system, functioning of receptors.

REFERENCES

TEXTBOOK
PAPER IV

**1BME4 : Biomedical Instrumentation**

**Unit-I**
- Fundamentals of Biomedical Instrumentation: Basic medical instrumentation system, Performance requirements of Medical Instrumentation System, Intelligent Medical Instrumentation System, biometrics.

**Unit-II**
- Bioelectric Signals and Electrodes: Origin of Bioelectric signals, Recording Electrodes, Silver-Silver Chloride Electrodes, Electrodes for ECG, EEG and EMG

**Unit-III**
- Biomedical Recorders: Electrocardiograph (ECG), vectorcardiograph (VCG), Phonocardiograph (PCG), Electroencephalograph (EEG), Electromyograph (EMG), cardiac pacemakers.

**Unit-IV**

**Unit-V**
- Radio-therapy Equipment: Use of high voltage X-ray Machines, Development of Betatron, Cobolt-60 Machine, Medical Linear Accelerator Machine, X-ray tomography, short wave, micro-wavand surgical diathermy.

**Text Book:**
R. S. Khandpur (2nd Addition) (McGraw Hill publication) : Biomedical Instrumentation

**REFERENCES:**

**LAB II**

**PRACTICALS:** Atleast 8 practicals to be performed by each student based on paper III & IV

SYLLABUS PRESCRIBED FOR P. G. DEGREE IN BIO-MEDICAL ELECTRONICS

Semester II

**PAPER V**

**2BME1 : SIGNALS SYSTEMS AND NETWORKS**

**UNIT-I**
- CLASSIFICATION OF SIGNALS AND SYSTEMS & ANALYSIS OF CT SIGNALS: Continuous time signals (CT signals), Discrete time signals (DT signals)- Step, Ramp, Pulse, Impulse, Exponential, Classification of CT and DT signals, Periodic and Aperiodic, random signals, CT systems and DT systems, Classification of systems - Linear Time invariant systems, Fourier series analysis, Spectrum of CT signals, Fourier Transform and Laplace Transform in Signal analysis.

**UNIT-II**

**UNIT-III**
- LT1 - DT SYSTEMS: Difference equations, Block diagram representation, Impulse response, Convolution SUM, Frequency response, FFT and Z-transform analysis, State variable equation and Matrix.

**UNIT-IV**

**UNIT-IV**
- ANALYSIS OF NETWORKS: Network elements, Transient response of RL, RC and RLC Circuits to DC excitation, Natural and forced Oscillations, Two-port Networks, Parameters and transfer function, Interconnection of two-ports.
UNIT-V: ELEMENTS OF NETWORK SYNTHESIS :- Network readability, Hurwitz polynomials, Positive real functions, Properties of RL, RC and LC Networks, Foster and Cauer forms of Realization, Transmission Zeros, synthesis of transfer functions

TEXT BOOKS

PAPER VI

2BME2: BIOMEDICAL INSTRUMENTATION

UNIT-I: BIO-POTENTIAL ELECTRODES: Electrode electrolyte interface, half-cell potential, polarisation and non-polarisable electrode, calomel electrode, needle and wire electrode, microelectrode-metal micropipette.

UNIT-II: RECORDING SYSTEM: Low-Noise preamplifier, main amplifier and driver amplifier, inkjet recorder, thermal array recorder, photographic recorder, magnetic tape recorder, X-Y recorder, medical oscilloscope.


UNIT-IV: NON-ELECTRICAL PARAMETER MEASUREMENTS: Respiration, heart rate, temperature, pulse blood pressure, cardiac output, O₂, CO₂ measurements.

UNIT-V: BLOOD FLOW AND BLOOD CELL COUNTING: Electromagnetic and ultrasonic blood flowmeter, indicator dilution method, thermodilution method, manual and automatic counting of RBC, WBC and platelets.

TEXTBOOK

REFERENCES

LAB III

PRACTICALS: Atleast 8 practicals to be performed by each student based on paper V & VI

PAPER VII

2BME3: SENSORS AND MEASURING TECHNIQUES

UNIT-I: CONCEPTS OF MEASUREMENT: Measurements, instrumentation, errors in measurements, calibration and standard.

TRANSDUCERS: Classification and characteristics of transducers, transducers for measurement of pressure, flow and temperature, optical sensors, acoustic sensors, DC and AC bridges.

UNIT-II: SIGNAL GENERATORS AND SIGNAL ANALYSERS: AF generator, Pulse generator, AM/FM signal generators, Function generator, Sweep frequency generator, wave analyser, spectrum analyser, logic analyser, distortion analyser.


UNIT-V: RECORDING SYSTEM: Analog and digital recorders, multichannel column display oscilloscope, magnetic recorder.

TEXTBOOK

**REFERENCES**

**PAPER VIII**

**2BME4 : MULTIMEDIA AND MEDICAL INFORMATICS**

**UNIT-I** : **MEDICAL DATABASE IMPLEMENTATION** :- Medical data acquisition and database systems: PC based multichannel data acquisition system; storage, analysis and retrieval techniques.

**UNIT-II** : **VISUAL BASIC** :- Visual programming concepts; visual Basic environment, tools and controls; Dynamic data exchange; VB based Medical information System.

**UNIT-III** : **COMPUTERS IN SYSTEM DESIGN** :- Hospital Information System its design and functional characteristics; Principles and application of Artificial Intelligence, Pattern Recognition, Neural Network and Fuzzy Logic in Medicine.

**UNIT-IV** : **MULTIMEDIA AND VIRTUAL REALITY APPLIED TO MEDICINE** :- Basic concepts of Multimedia; Design of Multimedia information systems; Components of virtual reality; Virtual reality applications in medicine.

**UNIT-V** : **COMPUTERS IN MEDICAL RESEARCH** :- Medical Informatics and its levels; Design and development of educational packages on medical sciences; Integrated design concepts; Interactive multimedia, Virtual and digital libraries, Internet and its applications.

**TEXTBOOK**


**REFERENCES**


**LAB IV**

**PRACTICALS** : Atleast 8 practicals to be performed by each student based on paper VII & VIII

**NOTE** : After completion of first semester, students have to complete either second semester or the on job training in company.

1. Marks on Academic Performance - 50
   a) Attendance - 10
   b) Knowledge of concepts - 10
   c) Intellectual Ability - 05
   d) Decision making ability - 05
   e) Skill for handling the task - 10
   f) Co-operation/leadership qualities - 05
   g) Sense of Responsibility - 05
2. Presentation and Demonstration of the Project Completed during training. - 100
3. Viva-voce (Internal at industries) - 50
4. Evaluation of Project (External) - 100

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**SYLLABUS PRESCRIBED FOR P.G. DIPLOMA IN COMPUTER MAINTENANCE**

**SEMESTER-I**

**PAPER I**

**1CPMS1 : Basic Analog Electronics**

**Unit I** : **Passive Components and Network theorems** : Introduction of Resistor, Capacitor, Inductor and Transformer. Concept of ideal dc voltage and current source, Statements of KVL, KCL, Thevenin, Nortons, maximum power transfer, Millman's and superposition theorems (proofs, simple numerical applicable for dc only).
UNIT II: Measuring Instruments: Principles of voltmeter, ammeter, ohmmeter, Multirange DC voltmeter, ohm per volt rating, loading effect, Multirange DC Ammeter, Series & shunt type ohmmeter, Multimeter, (uses & drawback), CRO Block diagram & explanation, CRT construction & working, uses of CRO (measurement of frequency, amplitude & phase).

Unit III: Semiconductor Diode and Regulated Power Supply: Operation and characteristics of PN junction, avalanche and Zener breakdown mechanism. Half wave and full wave rectifiers, ripple factor, efficiency, PIV ratings C, L and δ filters. Concept of unregulated and regulated power supply, Zener diode voltage regulator, Transistor series and shunt regulator, three terminal IC regulator.

Unit IV: Transistor: NPN and PNP transistor, (construction and working), CB, CE & CC configuration, leakage currents, Input and output characteristics of CE mode, relation between α and ã, Load line and operating point Amplification action of CE amplifier, biasing and stability, Self and fixed bias circuit.

Unit V: Memory: Concept of primary & secondary memory, memory hierarchy, classification of memories, Floppy disk, Winchester disk, CD, DVD. Semiconductor memories: RAM, ROM, PROM, EPROM, EAROM, and EEPROM.

Books Recommended:
1. Elements of Electronics by Bagade and Singh (S.Chand and company)
2. Electronic devices, application and integrated circuits by Mathur (Kulshrestha, Chadha, Umesh Publication)
3. Pulse, Digital, Switching wave forms by Millman and Taub (Mcgraw Hill-Kogakusha)
4. Basic Electronics by B.L. Theraja (S.Chand and company)
5. Electrical and electronic measurements and instrumentation by A.K. Sawhney (Dhanpat Rai and sons)
6. A text book of electrical technology B.L. Thereja (S.Chand & Company Ltd.)
7. Micro Electronic Circuits (Fourth Edition) by Sedra and Smith (Oxford publication)

PAPER-II
1 CPMS2: Basic Digital Electronics


Unit II: Boolean algebra & Logic families: Classification of logic families, characteristics (Fan-in, Fanout, Noise immunity, Propagation delay, Power dissipation), DTL, TTL, ECL & CMOS logic. Boolean laws, De-morgans theorem, Simplification of Boolean equations using boolean algebra Fundamental products & K-map(K-map upto 4 variable).

Unit III: Flip Flops & Counters: Construction & working of Astable, monostable and Bistable transistorised multivibrators, RS, CLK RS, D, JK, JKMS Flip Flops (Logic diagram, Truth table, construction & working), Concept of edge trigger Flip-Flop, Concept of preset & clear terminal. Asynchronous & synchronous Counter, Up-down counters (up to 4-bits), modified asynchronous counter.

Unit IV: Shift registers: Types of shift registers, SISO, SIPO, PISO & PIPO shift registers (Construction & working), left-shift right-shift registers, IC version of shift register 7495, Application of shift register. Ring counter, Johnson counter.


Books Recommended:
2. Digital and Analogue Techniques, Navneet / Kale / Gokhale, Kitab Mahal.

Lab.-I
Practicals: Atleast 8 practicals based on Paper-I & II.
PAPER III
1 CPMS3 : Computer Organization


Unit II : Processor : Structure of Instruction, Description of processor, CPU organization, Intel series of Microprocessor, Computer Architecture, Units, Processor to memory communication, I/O communications, Interrupts, Multiprogramming processor features, RISC, CISC.

Unit III : Memory and I/O devices: Main memory, Cache memory, Virtual memory, semiconductor memory, Memory controllers, Magnetic memory, Optical memory, Input units, output units, other units.

Unit IV : Computer Software : Introduction, System software, Programming Languages, Translator, Application Software, 4GL, Firmware, Middle ware, Popular Software packages.

Unit V : Operating systems: Need of OS, Types of OS, Introduction to DOS, Internal and External commands of DOS, BIOS and DOS, Interrupts, Interrupt services, DOS interrupts and functional Calls, Introduction to WINDOWs, Desktop, Start menu, Status bar, Program manager, system setting menu, Accessories, system maintenance tools, Introduction to UNIX.

Books :
1. Introduction to Computer s: Srivastav

Paper –IV
1 CPMS4 : Microprocessor & Interfacing

Unit-I : Introduction to microprocessor, Explanation to terms, Evolution of microprocessor, Microcomputer programming languages, Practical application, Microcomputer architecture, Single chip microprocessors, CU, ALU, Memory, I/O, Characteristics of Interrupts I/O, DMA, Coprocessor.

Unit-II : Microcomputer S/W concepts, Instruction formats, Addressing modes, Instruction types, ALP, 8085 mpu : Register structure, memory addressing, Interrupt system in 8085, Important features of 8086, 80486 and pentium.

Unit-III : Intel 86 : Introduction, Architecture, Register, Addressing modes, Data Xfers, Arithmetics, Bit manipulation, String, Unconditional Xfer, Conditional branch, Iteration control, Interrupt, Processor control, Assembler Dependent & Assembler pseudo instructions, I/O, IOP (8089), 86 Interrupts, 86 DMA.

Unit-IV : Peripheral Interfacing : Parallel vs Serial interfacing, Synch.
     & Asynch. data S transmission, USART, UART, keyboard/Display interfacing, cCassette recorder I/P Concept, CRT interfacing & CRT Controller, Printing interfacing, Printer controller chip, DMA controller.


Books Recommended:
1. Microprocessor Theory and Application (revised edition) - M.Raffiquellazammam
2. Microprocessor & interfacing : D.V.Hall.
5. Microprocessing System 8086/8088 :- Liu & Gibson.
6. IBM PC Assembly Language & Programming : Peter Abel.

Lab.-II

Practicals : Atleast 8 practicals based on Paper-III & IV

SYLLABUS PRESCRIBED FOR PG DIPLOMA IN COMPUTER MAINTENANCE
SEMESTER –II
PAPER-V
2 CPMS1 : C LANGUAGE WITH ALP

Unit-I : Introduction to C : Introduction, Simple C program, character set, Keywords, Identifier, Variables, Data types operators, Expressions, Statements, Functions.
Unit II: Conditional statements, Loops & functions: Relational operators, Logical operators, Conditional branching statements, conditional operators, Loops, Jumping functions, Anatomy of functions, Writing own functions, Function call.


Unit V: C language with ALP using BIOS & DOS functions: Procedures examples, Setting cursor, Clearing screen, Screen & K/B operation, Display on the screen, Display ASCII character set, Accepting input from K/B Display name.

Books Recommended:
2. ALP for PC: John Jocha, Peter Norton.
3. IBM PC ALP: Peter Abel

PAPER VI

2 CPMS2: DATA COMMUNICATION, NETWORKING & INTERNET


Unit II: Transmission Media: Guided Media (Twisted pair, Co-axial cable, Optical fiber), Unguided Media (Radio, VHF, microwave, satellite), Infrared Transmission, Fibre Optics Communication, Components (Source, Channel Detector).

Unit III: Data Modems: Concept of Modulation, Pulse Code Modulation (PCM), Shift Keying [ASK, FSK, PSK, QPSK, DPSK], Encoding techniques and CODEC, Classification of Modems, Standards and Protocols, Protocols used by Modem to Transfer files, Establishing a Connection.

Unit IV: Data Communication H/W: Network Architectures, Hosts, Clients, Circuit network configuration, Media, Communication devices, Digital transmission concept, Analog transmission concept, use of MODEM, Digital transmission of Analog data.

Unit V: Multichannel Data Communication: Circuits, channels and multichanneling, Multiplexing [FDM, TDM, CDM, WDM], Access Techniques (FDMA, TDMA, Spread Spectrum Techniques and CDMA), Digital hierarchies [SONET/SDH].

Books Recommended:

Lab-III

Practicals: Atleast 8 practicals based on Paper-V & VI.

PAPER VII

2 CPMS3: NETWORKING & INTERNET

Unit I: Networking Fundamentals: Telephone Communication, Voice Communication, Area Codes, Switches: Circuit switching, Digital switching, Network switching, PBX benefits, Cellular technology, FAX, ANI, IVR.

Unit II: Network Topologies: Bus Topologies, Examples of bus topology: Ethernet, LocalTalk, Ring Topologies, Examples of Token Ring Topology: IBM Token Ring, FOOI (Fiber Distributed Data Interface), Star Topologies, Example of Star Network: ATM (Asynchronous Transmission Mode).

Unit-IV : **Internet** : History, Growth, Owner, Anatomy, TCP/IP, IP address, Types of networks, LAN, WAN, C/S, Connectivity, www introduction, Servers, Browsers, http, IE, NN, Bookmarks, Cookies, E-mail, FTP, Telenet, Web publishing.

Unit-V : **Network Components** : Hub, Bridges, Routers, Switches, Gateways, VAST, Modems, E-Commerce, EDI-Electronic Data Interchange, ISDN-Elements, Uses of ISDN, Broadband ISDN, Network Security, firewall, VPN

Books Recommended :
1. Internet & Web Design - MacMillan.
2. Computer Networking - Ross

**PAPER-VIII**

2 CPMS4 : **PC MAINTENANCE & TROUBLE SHOOTING**

Unit-I : **Microcomputer System & Peripherals** : Computer Organization, Character & numbers, Codes, Memory, ALU, CU, IF, Interrupts, I/O, Device controllers, Error detection, MP, PC, K/B, CRT, Printer, Magnetic storage. Devices (FD, HD, MTD, OD), Special peripheral.

Unit-II : **IBM PC H/W overview** : Introduction (BIOX, DOS), PC family & H/W, System box, M/B, I/O & Interrupts, DMA, Peripherals interface & Controller, K/B interface, Parallel & Serial interface, CRT controller, FDC, HDC, Memory Refresh, POST.