

SANT GADGE BABA AMRAVATI UNIVERSITY GAZETTE

Official Publication of Sant Gadge Baba Amravati University



PART- ONE

Thursday, the 29th June, 2017

DIRECTION

No. 11/2017

Dated :- 28/06/2017

Subject :- Corrigendum to Direction Nos. 31 of 2011, 31 of 2012 & 3 of 2013 in respect of the Schemes of teaching & examination of III to VIII/X Semesters as per C.G.S. of various branches in the Faculty of Science & Technology.

Whereas, Direction Nos. 29 of 2010 & 30 of 2010 in respect of the Examinations leading to the Degree of B.E./B.Text.E./B.Tech.(Chem.Engg.) B.Tech.(Chem.Tech.) (Polymer) (Plastic) (Tech.) and the Examinations leading to the Degree of B.Tech. (Chem.Tech.) (Food, Pulp & Paper, Oil & Paint and Petrochemical Tech.) (Four year Degree Course... Semester Pattern.. Credit Grade System) are in existence,

AND

Whereas, Direction No. 31/2011 in respect of the Schemes of teaching & examination of Semesters III to VIII / X as per Credit Grade System of various branches in the Faculty of Science & Technology (the then Faculty of Engineering & Technology) is in existence,

AND

Whereas, Corrigendum to Direction No. 31 of 2011 i.e. Direction Nos. 31 of 2012 and 3 of 2013 regarding the revised Schemes of teaching & examination of Semester III to VIII in the various branches of B.E./B. Text./ B. Tech.(Chemical) / B.Tech. (Polymer)(Plastic) B.Tech. (Chem. Tech.) (Food, Pulp & Paper, Oil & Paint and Petrochemical) in the branches Production Engg., Electronics & Telecommn.Engg., Electronics, Instrumentation Engg., Computer Sci. & Engg., Computer Engg., Textile Engg., Chemical Engg., Chem. Tech. (Polymer) (Plastic) and Chem. Tech. (Food, Pulp & Paper, Oil & Paint and Petrochemical) are in existence,

AND

Whereas, the Board of Studies in Electronics, Industrial Electronics & Applied Electronics in its meetings held on 29/08/16, 25/10/16 and 17/05/2017 resolved to accept and recommend the Schemes of teaching & examinations and the draft syllabi of Semester III to VIII B.E. (Electronics & Telecommunications) for its implementation from the academic session 2017-2018 and onwards in phase wise manner,

AND

Whereas, Hon'ble Vice Chancellor has approved the draft Schemes of teaching & examinations and Draft Syllabi of Semester III to VIII B.E. (Electronics & Telecommunications) under Section 12(7) of the Maharashtra Public Universities Act, 2016 on behalf of the Faculty of Science & Technology for its implementation from the academic session 2017-2018 and onwards in phase wise manner,

AND

Whereas, the Academic Council in its meeting held on 06/06/2017 vide Item No.43 (1) has approved the Schemes of teaching & examinations and Draft Syllabi of Semester III to VIII B.E. (Electronics & Telecommunications),

AND

Whereas, the revised Schemes of teaching & examinations and the Syllabi of Sem. III to VIII B.E. (Electronics & Telecommunications) are to be implemented from the current academic session 2017-2018 in phase wise manner,

AND

Whereas, the Schemes of teaching & examinations of Semesters III to VIII of B.E. (Electronics & Telecommunications) along with the Syllabi are to be made available to the concerned from the academic session 2017-2018.

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AND

Whereas, the process of making an Ordinance / Regulation is a time consuming process,

Now, therefore, I, Dr. M.G. Chandekar, Vice-Chancellor of Sant Gadge Baba Amravati University, Amravati in exercise of powers conferred upon me under the sub-section (8) of Section 12 of the Maharashtra Public Universities Act, 2016, do hereby direct as under :-

This Direction shall be called "Corrigendum to Direction Nos. 31 of 2011, 31 of 2012 & 3 of 2013 in respect of the Schemes of teaching & examination of III to VIII/X Semesters as per C.G.S. of various branches in the Faculty of Science & Technology (the then Faculty of Engineering & Technology) Direction, 2017".

- 1) The Direction shall come into force from the date of its issuance.
- 2) This Direction shall come into force with effect from the academic session 2017-2018.
- 3) The revised Schemes of teaching and examination of Semesters III to VIII of B.E. (Electronics & Telecommunications) as per C.G.S. in the Faculty of Science & Technology shall be as per Appendices A, B, C, D, E & F appended herewith this Direction.

Date : 27/06/2017

sd/-
(Dr.M.G. Chandekar)
Vice-Chancellor
Sant Gadge Baba Amravati University

Four Year Degree Course in Bachelor of Engineering
Branch : **Electronics & Telecommunication Engineering**
Semester Pattern (Credit Grade System)

SEMESTER : THIRD																	
Sr. No.	Subject Code	Subject	TEACHING SCHEME					EXAMINATION SCHEME									
			HOURS / WEEK			Total HOURS/WEEK	CREDITS	THEORY					PRACTICAL				
			Lecture	Tutorial	P/D			Duration Of Paper (Hr.)	Max. Marks Theory Paper	Internal Marks	Total	Min. Passing Marks	Max. Marks		Total	Min. Passing Marks	
		Int.		Ext.													
THEORY																	
01	3ET1	Engineering Mathematics-III	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
02	3ET2	Object Oriented Programming	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
03	3ET3	Electronic Devices and Circuits	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
04	3ET4	Instrumentation and Sensors	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
05	3ET5	Electromagnetic Fields	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
06	3ET6	Environmental Science	2	--	--	2	--	--	--	--	--	--	-	-	-	-	
PRACTICALS / DRAWING / DESIGN																	
07	3ETp7	Object Oriented Programming - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
08	3ETp8	Electronic Devices and Circuits - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
09	3ETp9	Skill Development Lab-I (Measurements, Testing & Instrumentation)	2	--	2	4	2	--	--	--	--	--	50	50	100	25(Int.) 25(Ext)	
Total			24	2	6	32	26	--	--	--	500	--	--	--	200	--	
Total															700		

SEMESTER : FOURTH																	
Sr. No.	Subject Code	Subject	TEACHING SCHEME					EXAMINATION SCHEME									
			HOURS / WEEK			Total HOURS/WEEK	CREDITS	THEORY					PRACTICAL				
			Lecture	Tutorial	P/D			Duration Of Paper (Hr.)	Max. Marks Theory Paper	Internal Marks	Total	Min. Passing Marks	Max. Marks		Total	Min. Passing Marks	
Int.	Ext.																
THEORY																	
01	4ET1	Signals and Systems	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
02	4ET2	Network Analysis	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
03	4ET3	Analog Electronics-I	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
04	4ET4	Digital Electronics	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
05	4ET5	Communication Engg.-I	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
06	4ET6	Environmental Science	2	--	--	2	--	3	--	--	--	--	--	--	--	--	
PRACTICALS / DRAWING / DESIGN																	
07	4ETp7	Analog Electronics-I Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
08	4ETp8	Digital Electronics - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
09	4ETp9	Communication Engg.-I Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
10	4ETp10	Skill Development Lab-II (Software)	2	--	2	4	2	--	--	--	--	--	50	50	100	25(Int.) 25(Ext)	
Total			24	2	8	34	27	--	--	--	500	--	--	--	250	--	
Total															750		

SEMESTER : FIFTH																	
Sr. No.	Subject Code	Subject	TEACHING SCHEME					EXAMINATION SCHEME									
			HOURS / WEEK			Total HOURS/WEEK	CREDITS	THEORY					PRACTICAL				
			Lecture	Tutorial	P/D			Duration Of Paper (Hr.)	Max. Marks Theory Paper	Internal Marks	Total	Min. Passing Marks	Max. Marks		Total	Min. Passing Marks	
		Int.	Ext.														
THEORY																	
01	5ET1	Analog Electronics-II	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
02	5ET2	Power Electronics & Drives	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
03	5ET3	Micro Processor & Micro Controller	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
04	5ET4	Communication Engg.-II	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
05	FE5ET5	Free Elective-I (*)	3	--	--	3	3	3	80	20	100	40	--	--	--	--	
PRACTICALS / DRAWING / DESIGN																	
06	5ETp6	Analog Electronics-II Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
07	5ETp7	Power Electronics & Drives - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
08	5ETp8	Micro Processor & Micro Controller - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
09	5ETp9	Skill Development Lab-III (Simulation)	2	--	2	4	2	--	--	--	--	--	50	50	100	25(Int.) 25(Ext)	
Total			21	2	8	31	26	--	--	--	500	--	--	--	250	--	
Total															750		

Free Elective-I: (*)

1. Electronic Test Instruments
2. Fiber Optic & Satellite Communication

SEMESTER : SIXTH																	
Sr. No.	Subject Code	Subject	TEACHING SCHEME					EXAMINATION SCHEME									
			HOURS / WEEK			Total HOURS/WEEK	CREDITS	THEORY					PRACTICAL				
			Lecture	Tutorial	P/D			Duration Of Paper (Hr.)	Max. Marks Theory Paper	Internal Marks	Total	Min. Passing Marks	Max. Marks		Total	Min. Passing Marks	
		Int.		Ext.													
THEORY																	
01	6ET1	Micro Controller Programming & Applications	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
02	6ET2	Control Systems Engineering	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
03	6ET3	Digital Communication	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
04	6ET4	Digital Signal Processing	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
05	FE6ET5	Free Elective-II (*)	3	--	--	3	3	3	80	20	100	40	--	--	--	--	
06	6ET6	Communication Skills	2	--	--	2	--	--	40	10	50	20	--	--	--	--	
PRACTICALS / DRAWING / DESIGN																	
07	6ETp7	Digital Communication- Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
08	6ETp8	Digital Signal Processing -Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
09	6ETp9	Communication Skills - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
10	6ETp10	Skill Development Lab-IV (Hardware)	2	--	2	4	2	--	--	--	--	--	50	50	100	25(Int.) 25(Ext)	
Total			23	2	8	33	26	--	--	--	550	--	--	--	250	--	
Total															800		

Free Elective-II : (*)

1. Consumer Electronics

2. Introduction to Wireless Technology

SEMESTER : SEVENTH																	
Sr. No.	Subject Code	Subject	TEACHING SCHEME					EXAMINATION SCHEME									
			HOURS / WEEK			Total HOURS/WEEK	CREDITS	THEORY					PRACTICAL				
			Lecture	Tutorial	P/D			Duration Of Paper (Hr.)	Max. Marks Theory Paper	Internal Marks	Total	Min. Passing Marks	Max. Marks		Total	Min. Passing Marks	
														Int.	Ext.		
THEORY																	
01	7ET1	VLSI Design	4	1	--	5	5	3	80	20	100	40	--	--	--	--	
02	7ET2	Digital Image Processing	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
03	7ET3	Satellite & Optical Fiber Communication	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
04	7ET4	Industrial Management & Quality Control	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
05	7ET5	Professional Elective-I	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
PRACTICALS / DRAWING / DESIGN																	
06	7ETp6	VLSI Design - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
07	7ETp7	Skill Development Lab-V (Signal & Image Processing)	2	--	2	4	2	--	--	--	--	--	50	50	100	25(Int.) 25(Ext)	
08	7ETp8	Project	--	--	2	2	--	--	--	--	--	--	--	--	--	--	
09	7ETp9	Seminar	--	--	2	2	2	--	--	--	--	--	50	--	50	25	
Total			22	1	8	31	26	--	--	--	500	--	--	--	200	--	
														Total		700	

Professional Elective-I:

- | | | |
|--|----------------------|--------------------------|
| 1. Computer Organization | 2. PLC & Automation | 3. Smart Sensors |
| 4. Fuzzy Logic & Artificial Neural Network | 5. Speech Processing | 6. RF Modeling & Antenna |

SEMESTER : EIGHTH																	
Sr. No.	Subject Code	Subject	TEACHING SCHEME					EXAMINATION SCHEME									
			HOURS / WEEK			Total HOURS/WEEK	CREDITS	THEORY					PRACTICAL				
			Lecture	Tutorial	P/D			Duration Of Paper (Hr.)	Max. Marks Theory Paper	Internal Marks	Total	Min. Passing Marks	Max. Marks		Total	Min. Passing Marks	
THEORY																	
01	8ET1	UHF & Microwaves	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
02	8ET2	Wireless Communication	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
03	8ET3	Data Communication Network	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
04	8ET4	Professional Elective-II	4	--	--	4	4	3	80	20	100	40	--	--	--	--	
PRACTICALS / DRAWING / DESIGN																	
05	8ETp5	UHF & Microwaves - Lab	--	--	2	2	1	--	--	--	--	--	25	25	50	25	
06	8ETp6	Skill Development Lab -VI (Networking)	2	--	2	4	2	--	--	--	--	--	50	50	100	25(Int.) 25(Ext)	
07	8ETp7	Project	--	--	4	4	6	--	--	--	--	--	75	75	150	75	
Total			18	--	8	26	25	--	--	--	400	--	--	--	300	--	
Total															700		

Professional Elective-II:

- | | | |
|---------------------------|----------------------------------|-----------------------------------|
| 1. Embedded System & RTOS | 2. Automotive Electronics | 3. Wireless Sensor Network |
| 4. Biomedical Engineering | 5. Data Compression & Encryption | 6. Ultra Wide Band Communications |